BOARD OF TECHNICAL EDUCATION, DELHI

No. F. 53(0)/Exam/BTE/2025

Dated : 29/07/25

Electronic Engineering(Digital Electronics & Microprocessor System Design)

Performance of the candidates (Compartment) of IV semester of three years Diploma Course in Electronic Engineering(Digital Electronics & Microprocessor System who have appeared for the examination held in JUN 2025 is communicated as under. The marks of candidates who have cleared full subjects out of an aggregate of 1200 are indicated against their roll numbers.

If there is any discrepency, that should be brought to the knowledge of Board within FIFTEEN DAYS from the date of publication of result. NO CASES WILL BE ENTERTAINED AT LATER STAGE.

Rall No	Subjects in which declared passed / Marks Obtained
2221071004	
2221071006	•
2221071007	ED471, ED472, ED473, ED480, ED481, ED482, ED483, ED484, ED485.
2221071008	ED470, ED480, ED481, ED482, ED483, ED484, ED485.
2221071012	
2221071014	ED472, ED473, ED480, ED481, ED482, ED483, ED484, ED485
: 2221071015	
2221071016	ED470, ED472, ED473, ED480, ED481, ED482, ED483, ED484, ED485.
2221071022	
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NOTE :

- 1. Eligible candidates who have not been successful in any of the subject or have not appeared in any subject are notified as failed
- 2. The performance of the candidates who have been placed under P.L.O. will be communicated separately, if necessary.

SUBJECT CODES :

Theories

ED470 Electronic Devices and Circuits-III

ED471 Introduction to Microprocessors

ED472 Electronics Instruments & Measurements

ED473 Personal Computer Organisation

Practicals

ED480 Electronic Devices and Circuits-III

Introduction to Microprocessors ED481

Electronics Instruments & Measurements

ED483 Personal Computer Organisation ED484

Electronic Design and Drawing

ED485 Minor Project

Forwarded to the Principal, Chhoto Ram Roral Institute of Technology, Kanjhawla, Delhi for information and necessary action.

> (S. P. MEHER) CONTROLLER